



REPLACEMENT SHEET

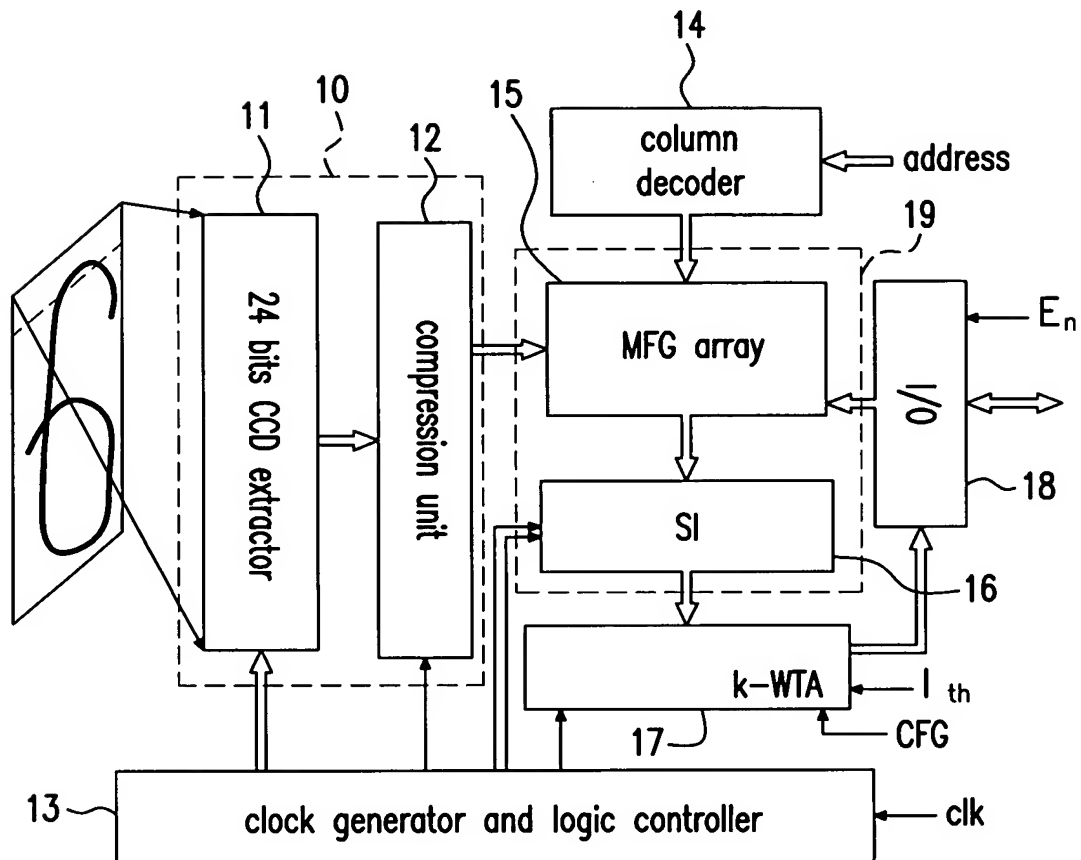


FIG. 1

The circuit diagram shows a differential signal processing circuit. It features a central cross-coupled core with two input nodes, 20 and 22, and two output nodes, 21 and 23. The core is biased by a current source ST connected to node 20 and a current source S connected to node 22. The output nodes 21 and 23 are connected to the input nodes 20 and 22 via feedback paths 24, 25, 26, and 27. The input signal IN is applied to the top of the core, and the output signal OUT is taken from the bottom of the core. The circuit is biased by a current source ST connected to node 20 and a current source S connected to node 22. The output nodes 21 and 23 are connected to the input nodes 20 and 22 via feedback paths 24, 25, 26, and 27. The input signal IN is applied to the top of the core, and the output signal OUT is taken from the bottom of the core.

FIG. 2

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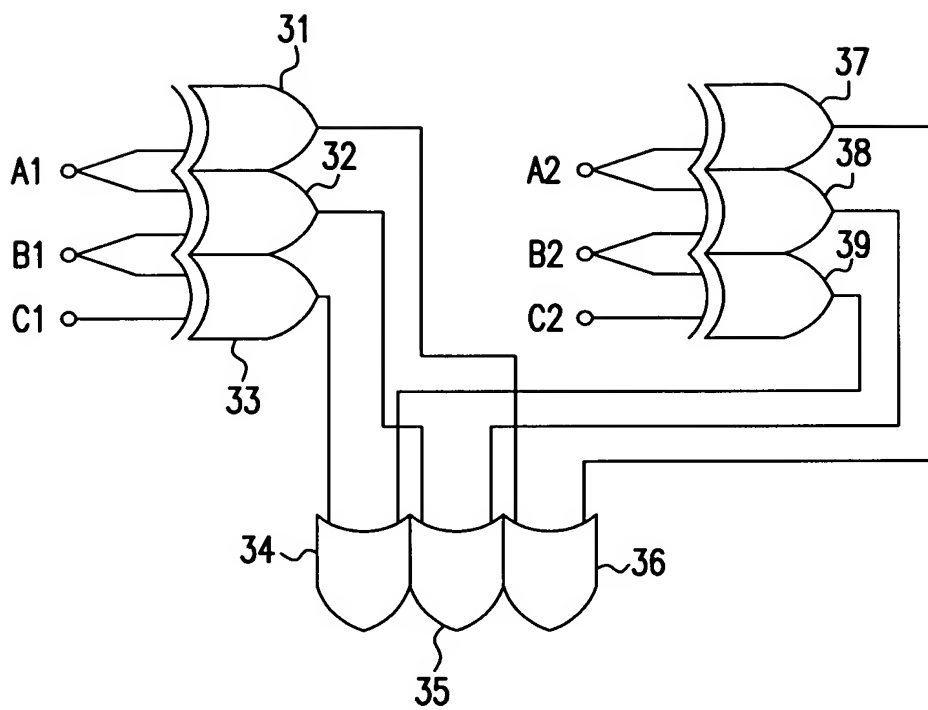


FIG. 3

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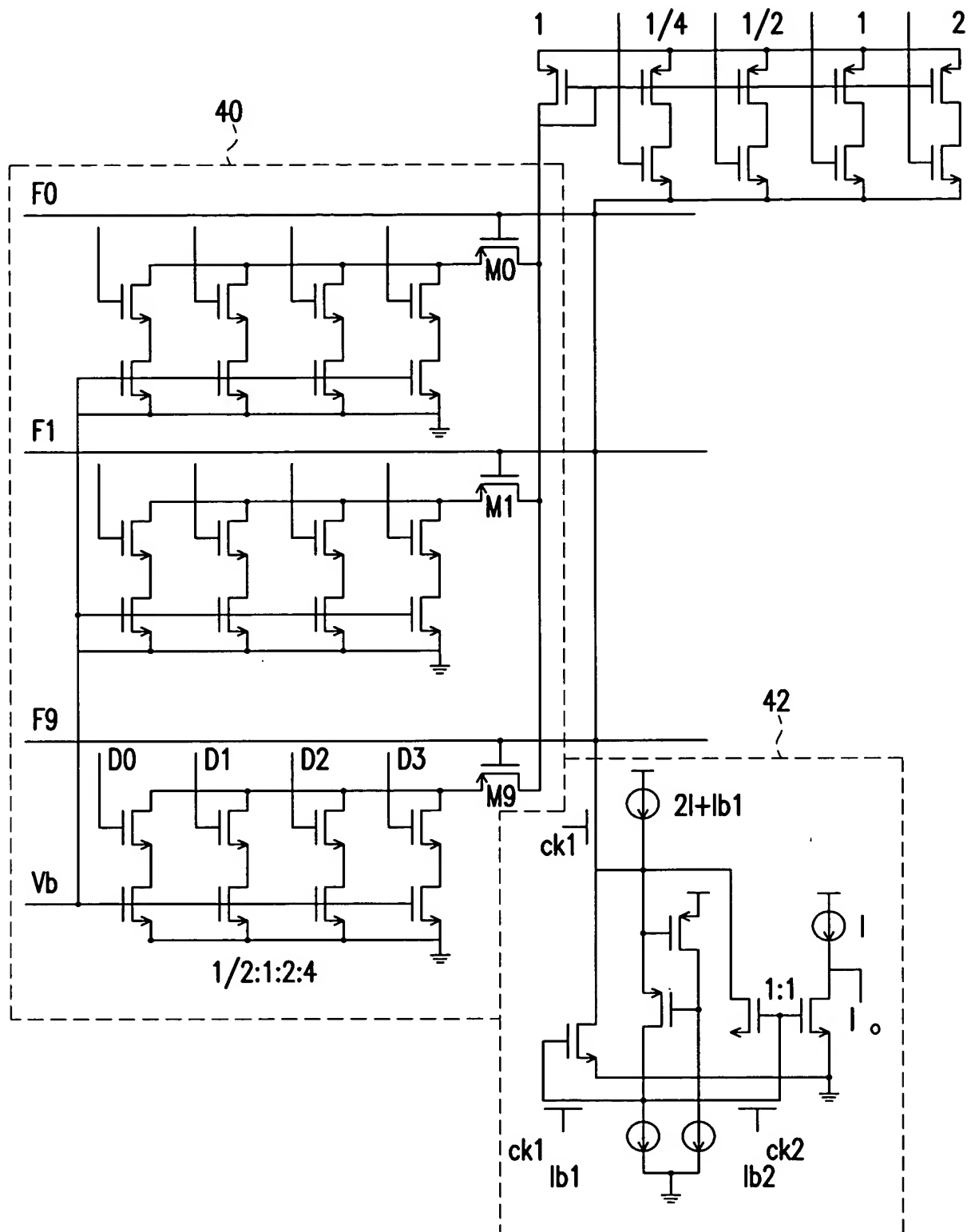


FIG. 4

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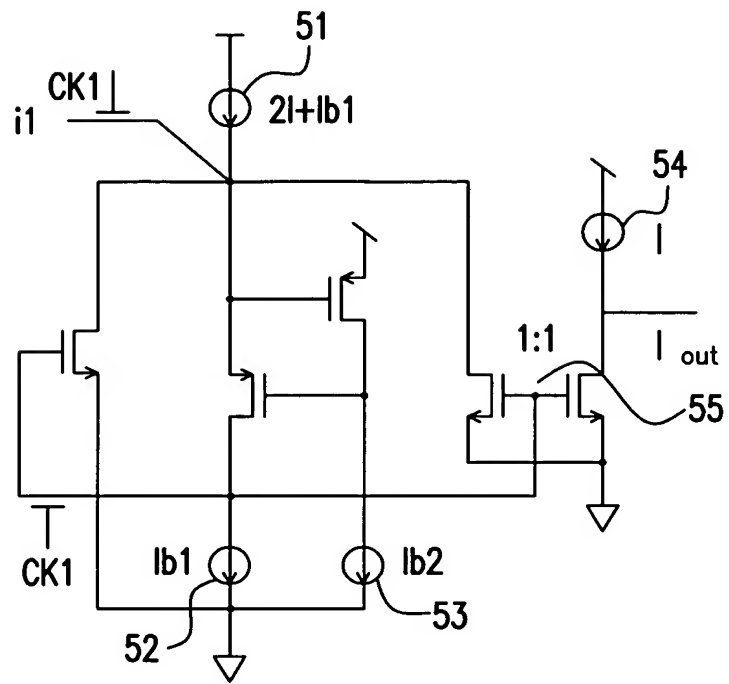


FIG. 5

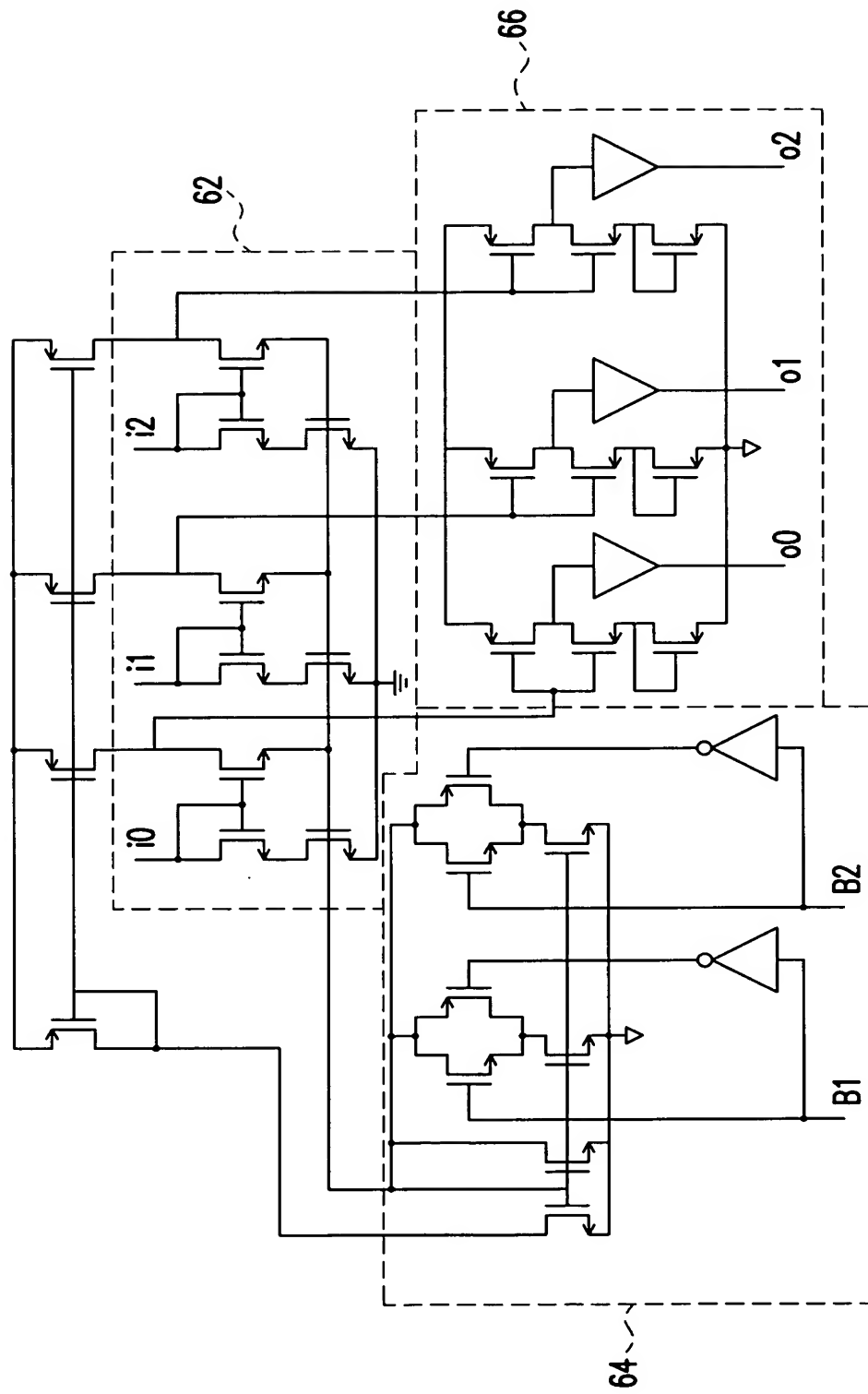


FIG. 6